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Sheet	1	of	2	Application Number	10/037,666
				Filing Date	January 3, 2002
				First Named Inventor	Sriram Vajapeyam
				Art Unit	2183
				Examiner Name	David I. Huisman
				Attorney Docket Number	42390P11927

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	/David Huisman/	Date Considered	05/17/2006
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**Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.*

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Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (w1) 08/11/2003.

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Sheet

2

of

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ¹
DJH		Black, Bryan, et al., "The Block-based Trace Cache", Proceedings of the 26th International Symposium on Computer Architecture, pp. 196-207 (May 2-4, 1999)	
DJH		Smith, James E., et al., "Trace Processors: Moving to Fourth-Generation Microarchitectures", Computer, Vol. 30, Issue 9, pp. 68-74 (Sep. 1997).	
DJH		Vajapeyam, Sriram, et al., "Dynamic Vectorization: A Mechanism for Exploiting Far-Flung ILP in Ordinary Programs", Proceedings of the 26th International Symposium on Computer Architecture, pp. 16-27 (May 2-4, 1999)	
DJH		U.S. Application No. 10/950,693, entitled SYSTEM, METHOD AND APPARATUS FOR DEPENDENCY CHAIN PROCESSING, filed September 28, 2004, by Satish Narayanasamy et al.	

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